



CA4/2824

PATENT
81788.0026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Seiichi MORI ✓

Serial No: 09/456,873 ✓

Filed: December 8, 1999 ✓

For: Non-Volatile Semiconductor Memory
Device ✓

Art Unit: 2824 ✓

Examiner: M. Carter

#5
S. Carter
HDS-8

**TRANSMITTAL OF INFORMATION DISCLOSURE
STATEMENT**

Assistant Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

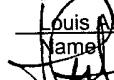
Assistant Commissioner for Patents
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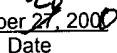
Louis A. Mok

Name



September 27, 2000

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Date

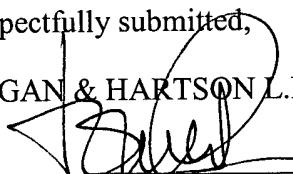
Dear Sir:

The information disclosure statement submitted herewith is being filed before the first action on the merits of this application.

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By: 

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